

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants	:	Taylor et al.)	I hereby certify that this paper is
U.S. Serial No.	:	10/829,495)	being filed electronically with the
Filed	:	April 22, 2004)	U.S. Patent and Trademark Office
Title	:	"Method and System for Fail-)	on this date:
		Safe Renaming of Logical))
		Circuit Identifiers for))
		Rerouted Logical Circuits in a)	May 26, 2009
		Data Network"))
))
))
Art Unit	:	2419)	<u>/Felipe Hernandez/</u>
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RESPONSE TO THE FINAL OFFICE ACTION DATED MARCH 23, 2009

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the final action dated March 23, 2009, please consider the following remarks.

A status of the claims is reflected in the listing of the claims beginning on page 2 of this paper.

Remarks begin on page 8 of this paper.